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APPLICANTS Kazutaka Manabe, Tokyo, JAPAN; <i>DM</i>					
** CONTINUING DATA ***** This application is a DIV of 10/106,642 03/26/2002 PAT 6,638,801 <i>DM</i>					
** FOREIGN APPLICATIONS ***** <i>DM</i> JAPAN 2001-096924 03/29/2001					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 10/14/2003					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged: <i>Darryl G. Walker</i> Examiner's Signature <i>DM</i> Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 3
ADDRESS Darryl G. Walker WALKER & SAKO, LLP Suite 235 300 South First Street San Jose, CA 95113					
TITLE Semiconductor device and its manufacturing method					
FILING FEE RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		